

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

ANDREAS DICKS

DE 010057

Serial No.

Group Art Unit

Filed: CONCURRENTLY

Ex.

Title: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE AND A
SEMICONDUCTOR DEVICE OBTAINED BY MANES OF SAID METHOD

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination, please
amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

4. (amended) An integrated circuit as claimed in Claim 1,
characterized in that, in the handshake method the function block
(5) acting as a slave, sends an acknowledge message (13) to the
function block (4) acting as master, after each permitted and
finalized access of the function block acting as master to the
linear address ranges.

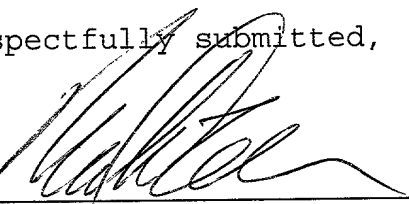
5. (amended) An integrated circuit as claimed in Claim 1, characterized in that, in the handshake method the function block (5) acting as a slave sends an abort-try again acknowledge message (15) to the function block (4) acting as master after each rejected access of the function block (4) acting as master to the linear address ranges.

REMARKS

The foregoing amendments to the claims were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserve all rights he may have under the Doctrine of Equivalents. Applicant furthermore reserves his right to reintroduce subject matter deleted herein at a later time during the prosecution of this application or continuing applications.

Respectfully submitted,

By 
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APPENDIX

4. (amended) An integrated circuit as claimed in Claim 1-~~or 2~~, characterized in that, in the handshake method the function block (5) acting as a slave, sends an acknowledge message (13) to the function block (4) acting as master, after each permitted and finalized access of the function block acting as master to the linear address ranges.

5. (amended) An integrated circuit as claimed in Claim 1-~~or 2~~, characterized in that, in the handshake method the function block (5) acting as a slave sends an abort-try again acknowledge message (15) to the function block (4) acting as master after each rejected access of the function block (4) acting as master to the linear address ranges.